



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ahn et al. Examiner: Clark, Sheila V.
Serial No: 10/054,700 Group Art Unit: 2815
Filed: January 17, 2002 Docket: 8028-17 (SPX2001100008US)
For: **PAD ARRANGEMENT IN SEMICONDUCTOR MEMORY
DEVICE AND METHOD OF DRIVING SEMICONDUCTOR
DEVICE**

Mail Stop Non Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

In response to the Office Action dated October 6, 2003, please amend the above-identified application as follows:

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TECHNOLOGY CENTER 2800

CERTIFICATE OF MAILING 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on January 6, 2004.

Dated: 1/6/04


Frank Chau